

Fig. 1
PRIOR ART

Docket No. 2003P52599US/I331.101.101
MEMORY DEVICE HAVING MULTIPLE ARRAY STRUCTURE FOR INCREASED BANDWIDTH

Jong-Hoon Oh
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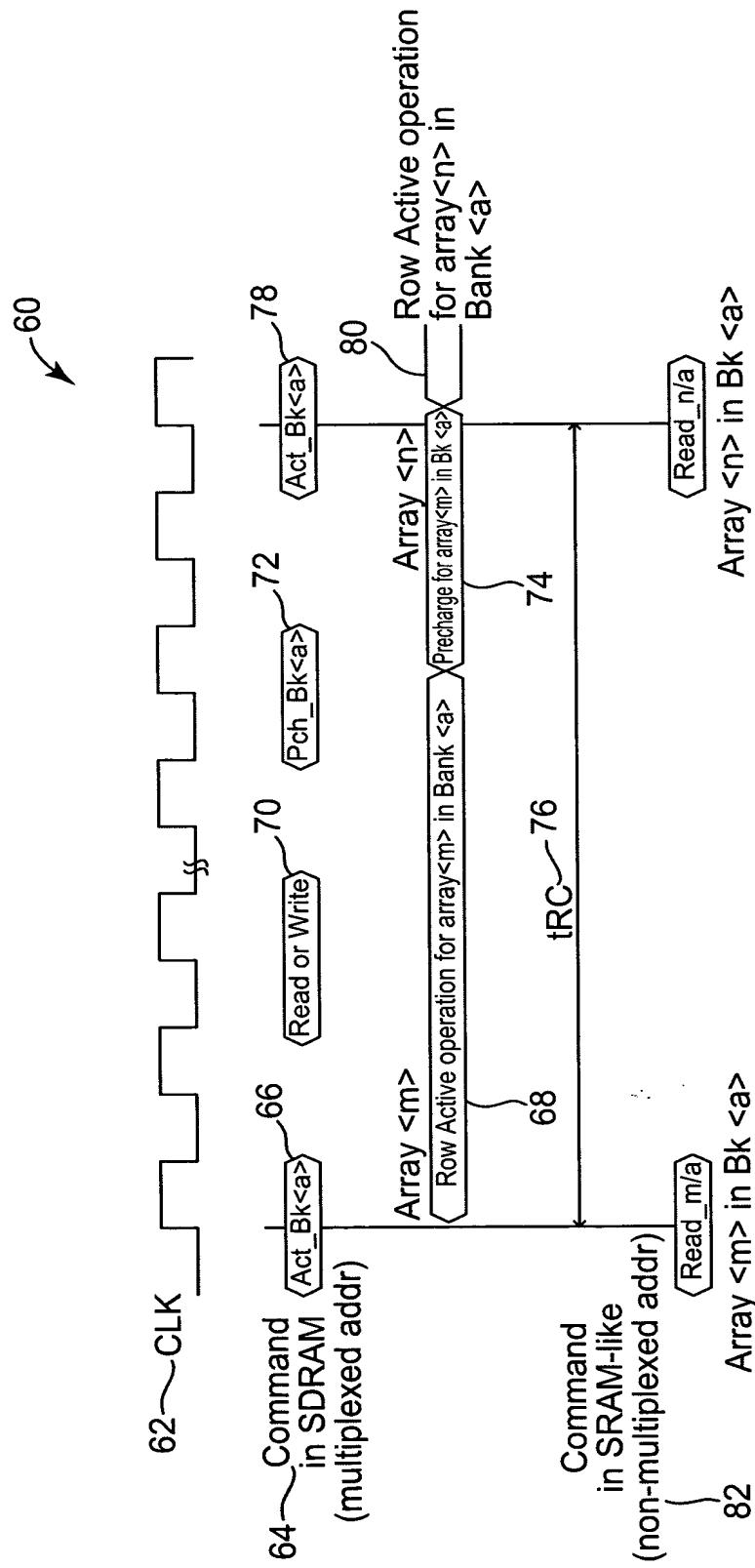


Fig. 2
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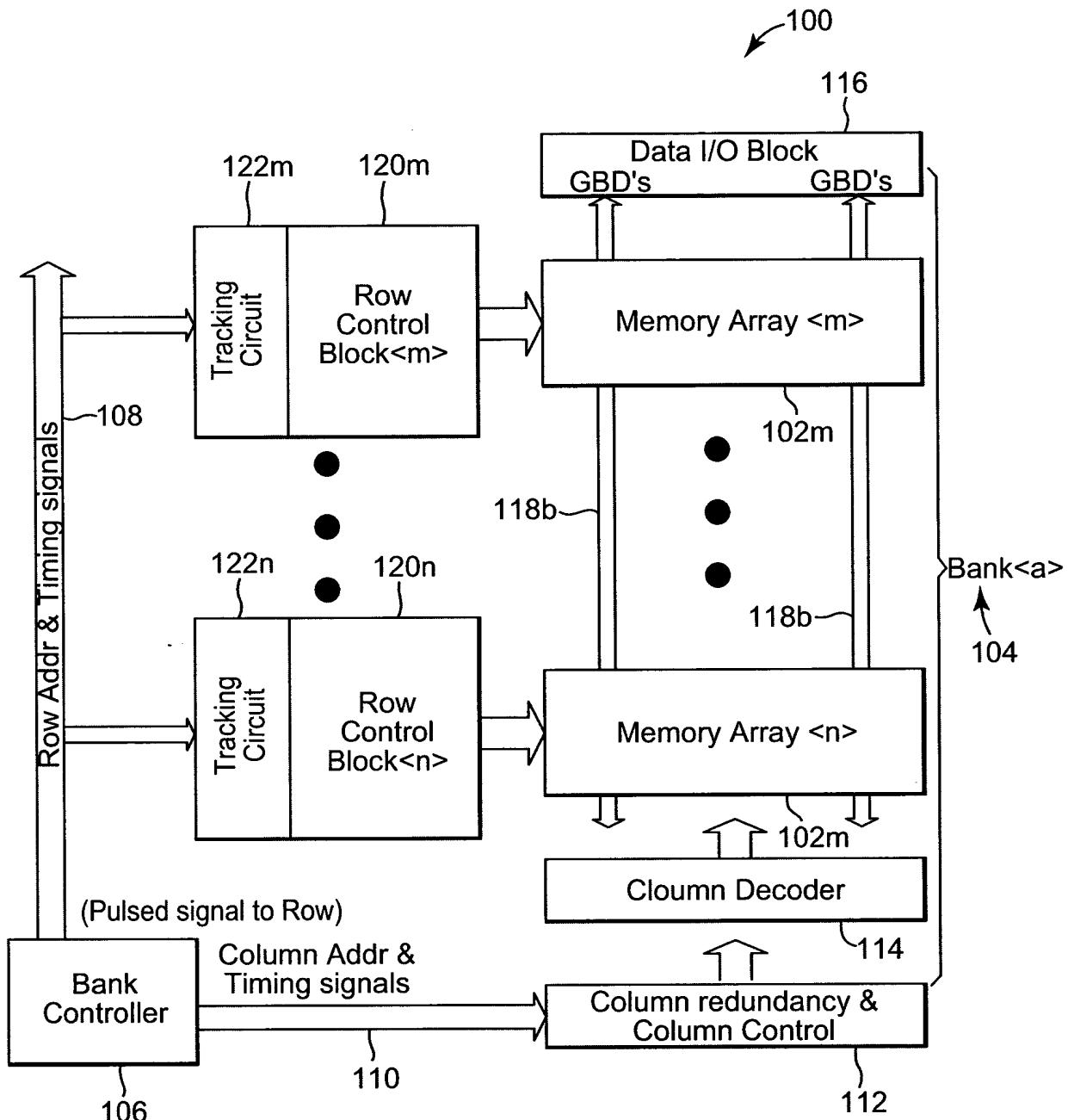


Fig. 3

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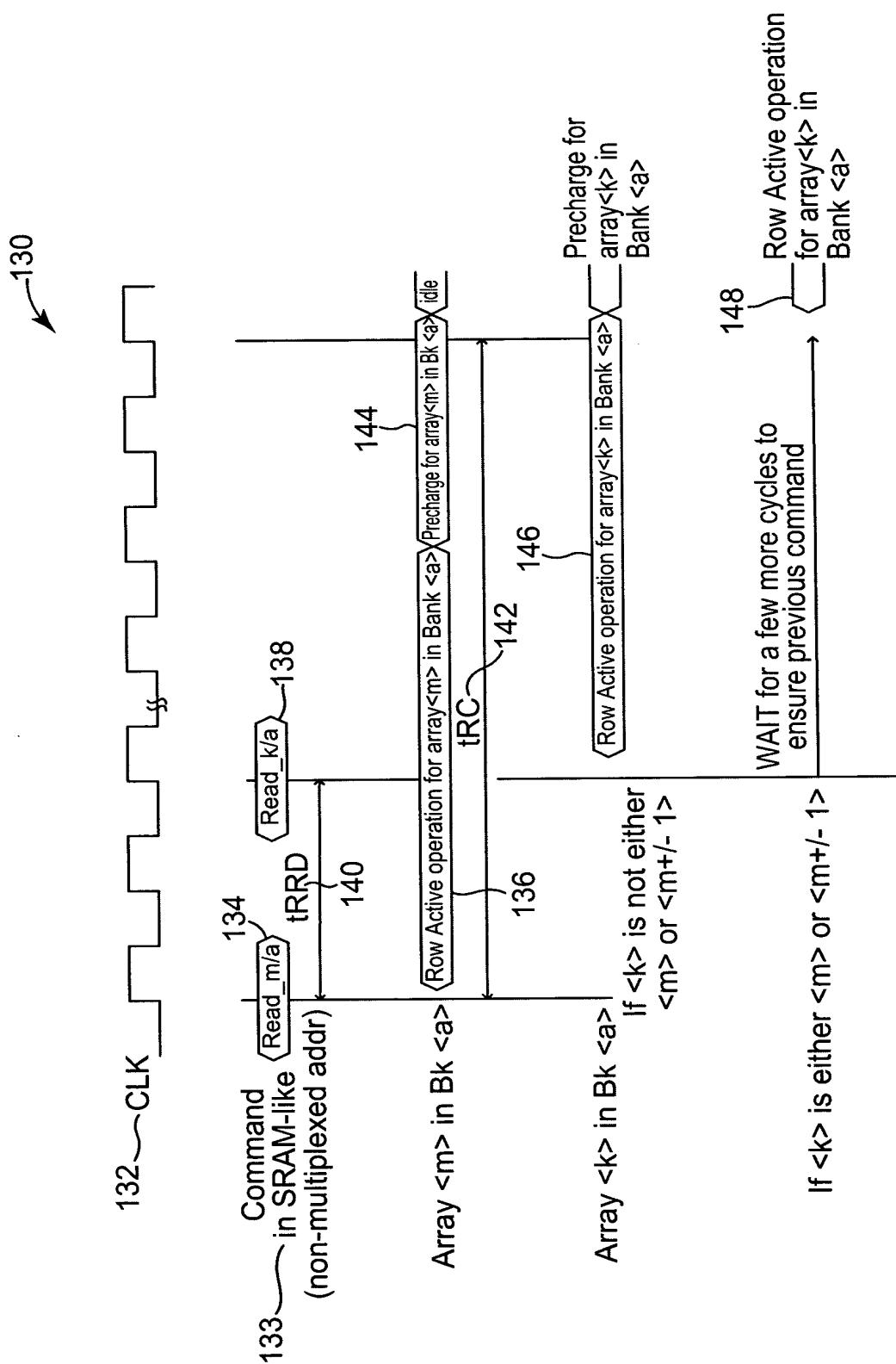


Fig. 4

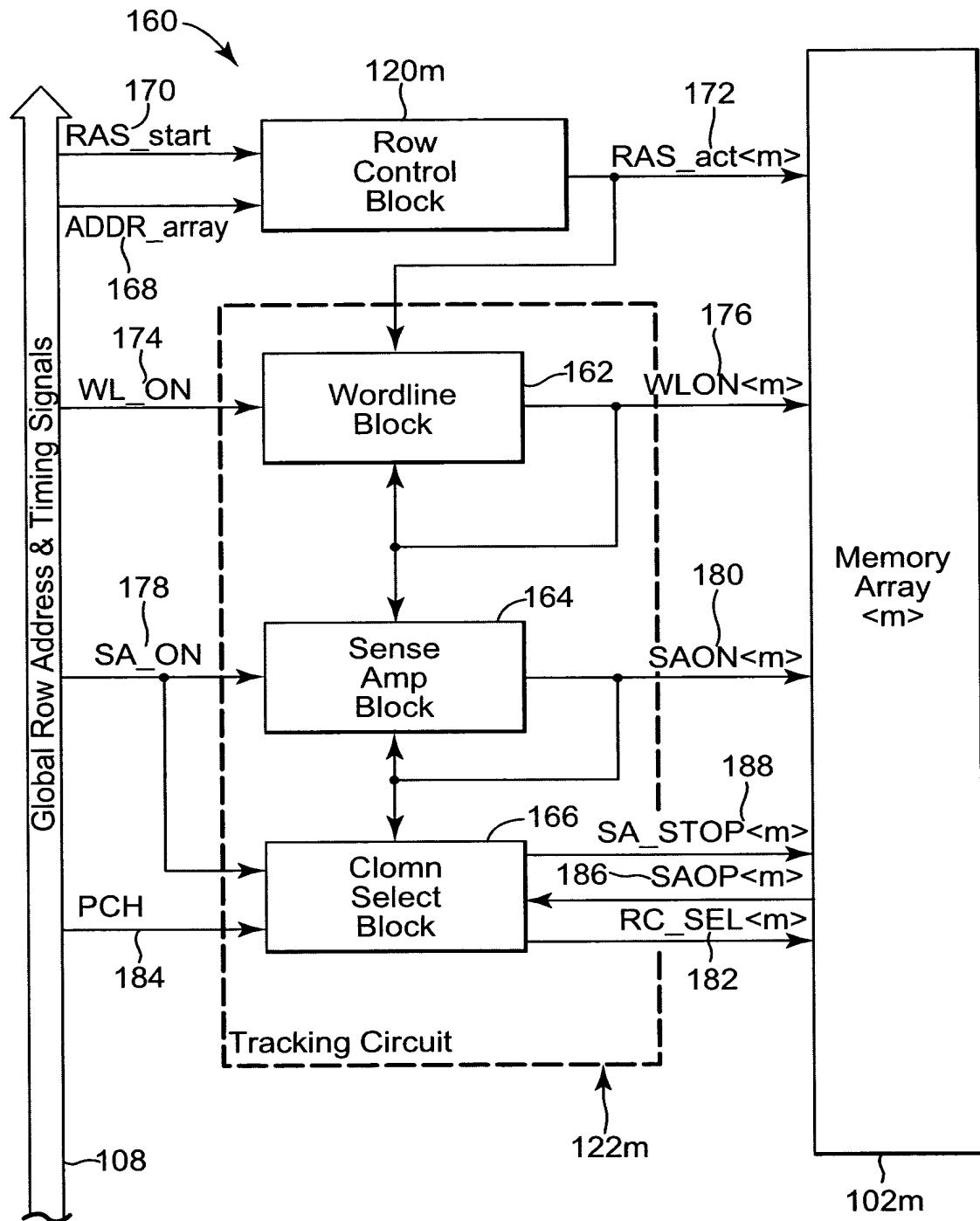


Fig. 5

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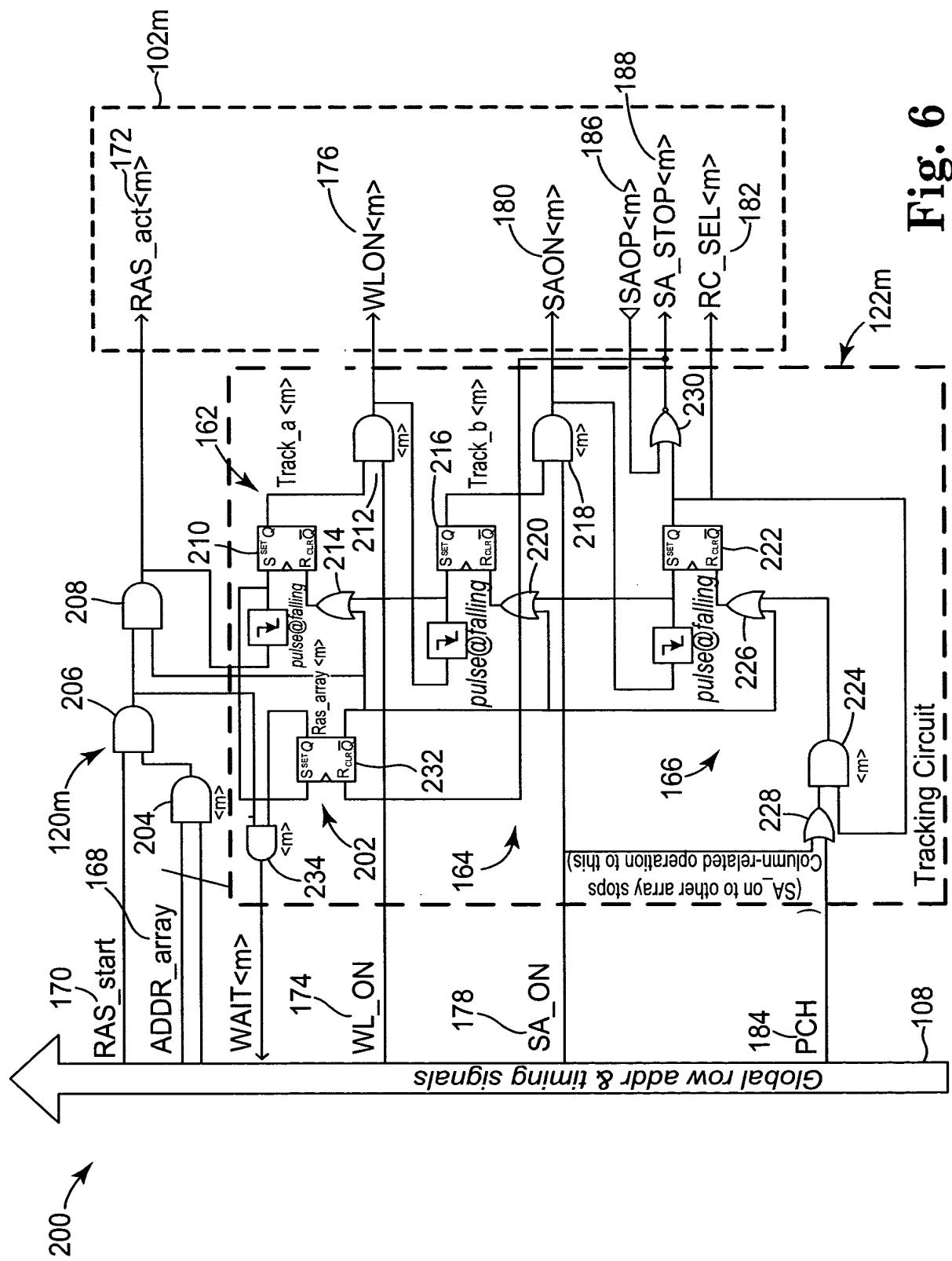


Fig. 6

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 STRUCTURE FOR INCREASED BANDWIDTH**

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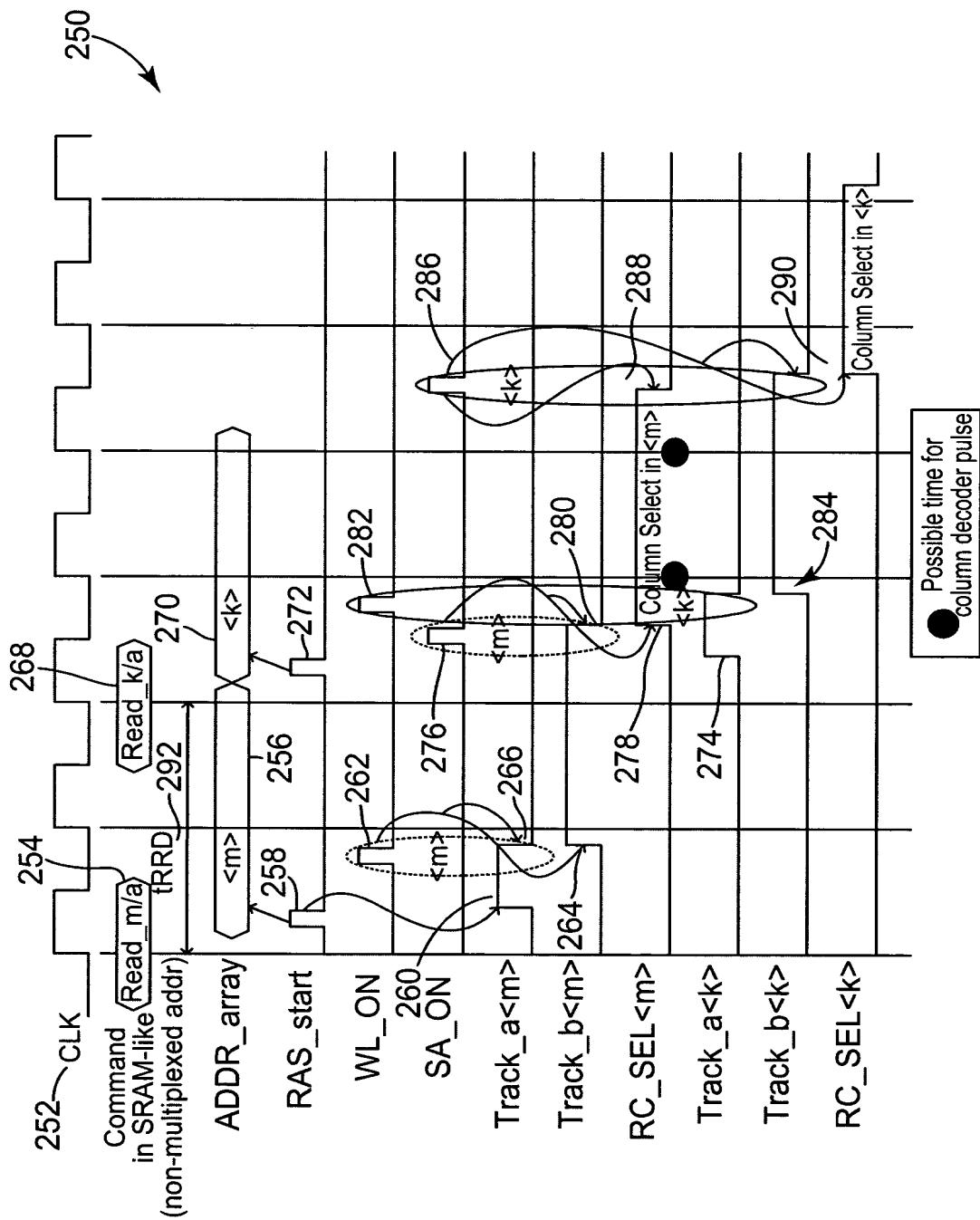


Fig. 7A

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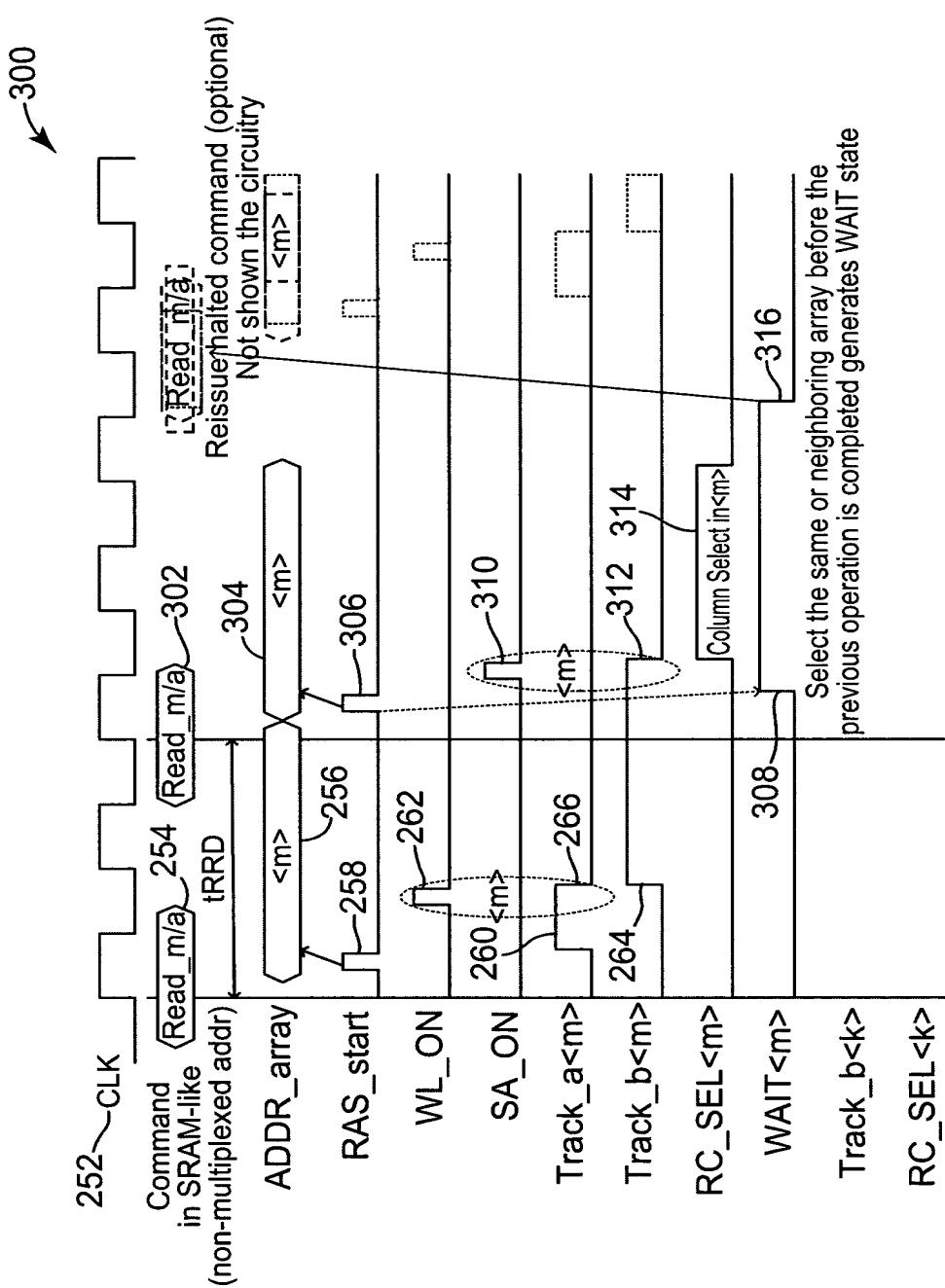


Fig. 7B

